



IN THE UNITED STATES

PATENT AND TRADEMARK OFFICE

APPLICANTS: James D. Kelly and Michael L. Regal
APPLICATION NO.: 10/669,119 (REI OF 5,996,036)
FILING DATE: SEPTEMBER 22, 2003
TITLE: BUS TRANSACTION REORDERING IN A COMPUTER SYSTEM HAVING UNORDERED SLAVES
EXAMINER: Gopal C. Ray
GROUP ART UNIT: 2111
ATTY. DKT. NO.: 18602-08098 (P2080R1C1)

CERTIFICATE OF MAILING			
I hereby certify that this correspondence, including the enclosures identified above, is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Reissue, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below. If the Express Mail Mailing Number is filled in below, then this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service pursuant to 37 CFR 1.10.			
Signature:	<i>Laura Majerus</i>		
Typed or Printed Name:	Laura A. Majerus, Reg. No. 33,417	Dated:	December 23, 2004
Express Mail Mailing Number (optional):	EV 441504220 US		

MAIL STOP REISSUE
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

AMENDMENT A

IN THE ABSTRACT

Replace the Abstract with the following:

[[A mechanism is provided for reordering]] Reordering bus transactions [[to increase]] increases bus utilization [[in a computer system in which]] where a split-transaction bus is bridged to a single-envelope bus. In one embodiment, both masters and slaves are ordered[[, simplifying implementation. In]];in another embodiment, [[the system is more loosely coupled with]] only masters [[being]] are ordered. Greater bus utilization is thereby achieved. To avoid